

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Johannes Becker

Serial No.: 10/706,365

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Title: SYSTEM AND METHOD FOR SECURING AN INTEGRATED CIRCUIT  
AS AGAINST SUBSEQUENT REPROGRAMMING

Grp./A.U.: 2135

Examiner: Suman Debnath

Confirmation No.: 6816

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed  
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(Signature of the person signing the certificate)

Sir:

**AMENDMENT UNDER 37 C.F.R. §1.116**

The Applicant has carefully considered this application in connection with the final Examiner's Action electronically sent August 8, 2008. The Applicant respectfully requests reconsideration of this application in view of the following amendment and remarks.